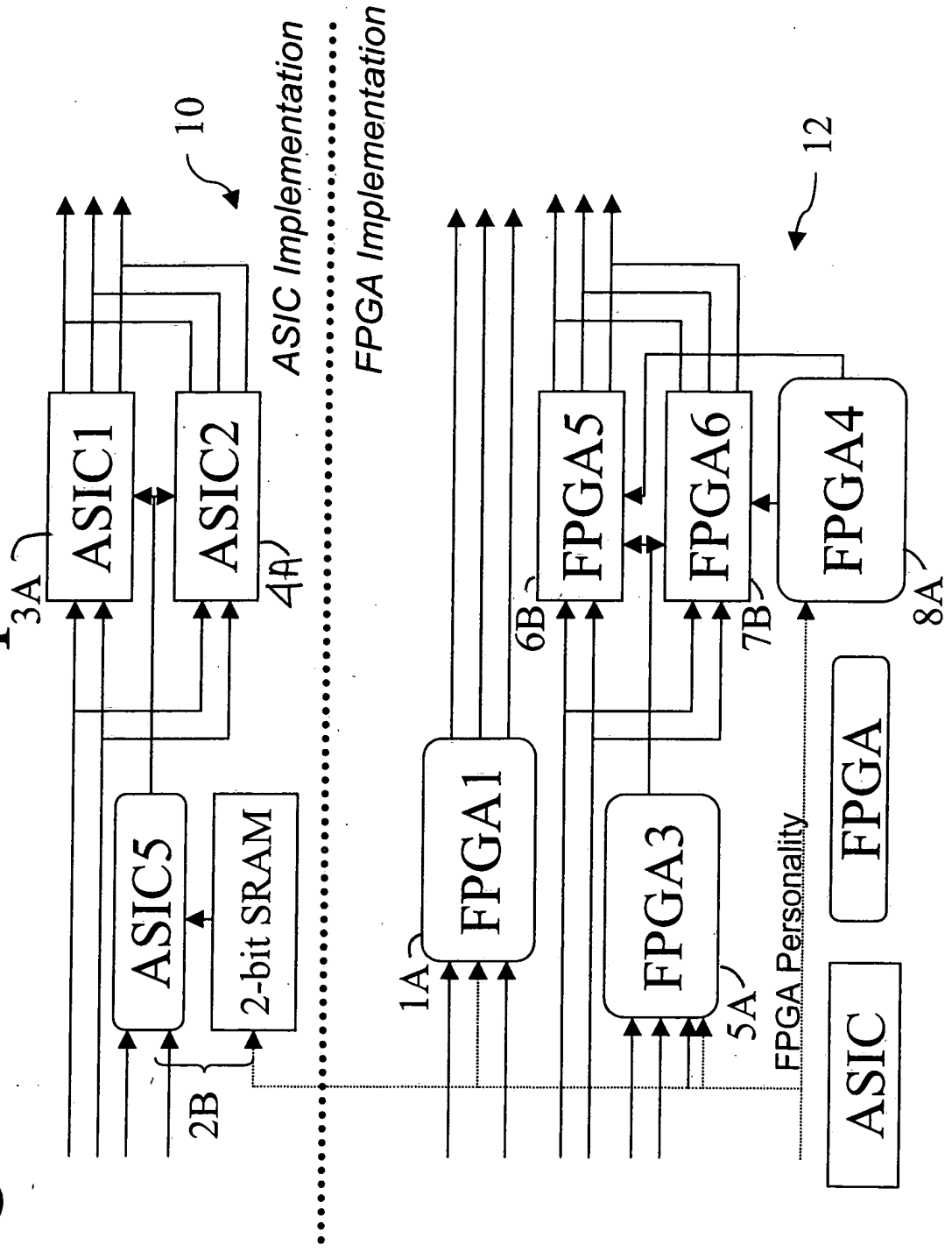


Figure 4 Final Implementation



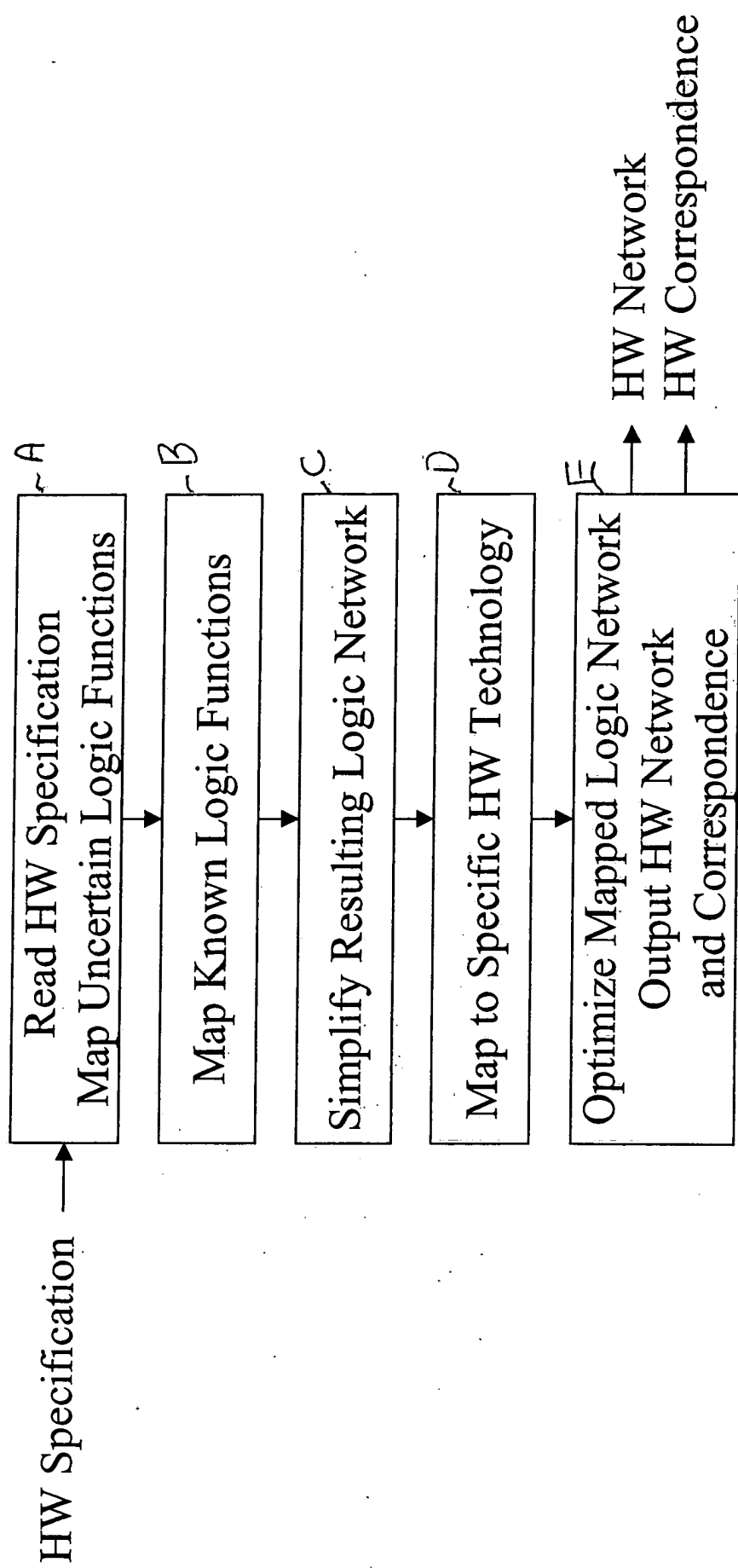
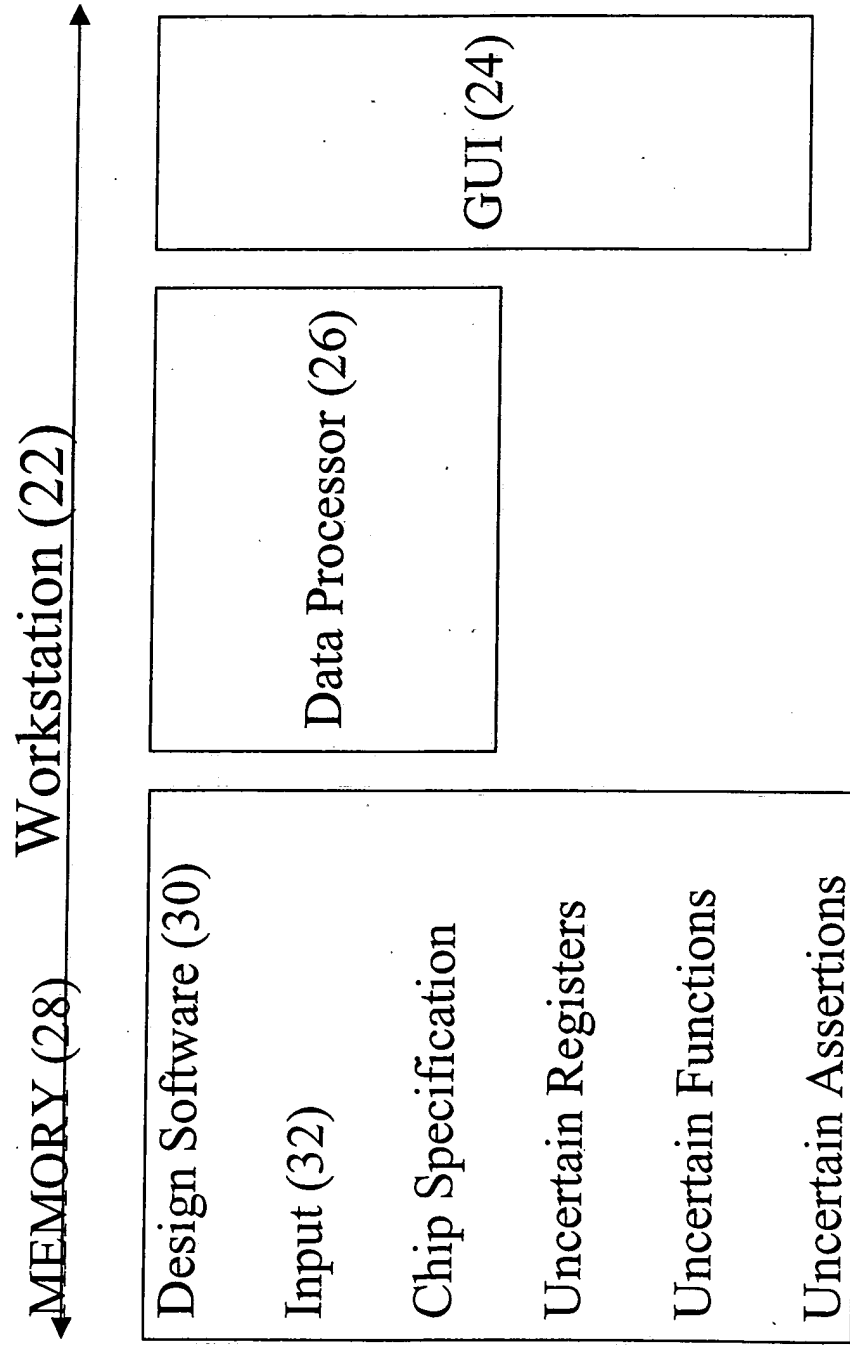


Figure 5



Output Network (34) Figure 6 205

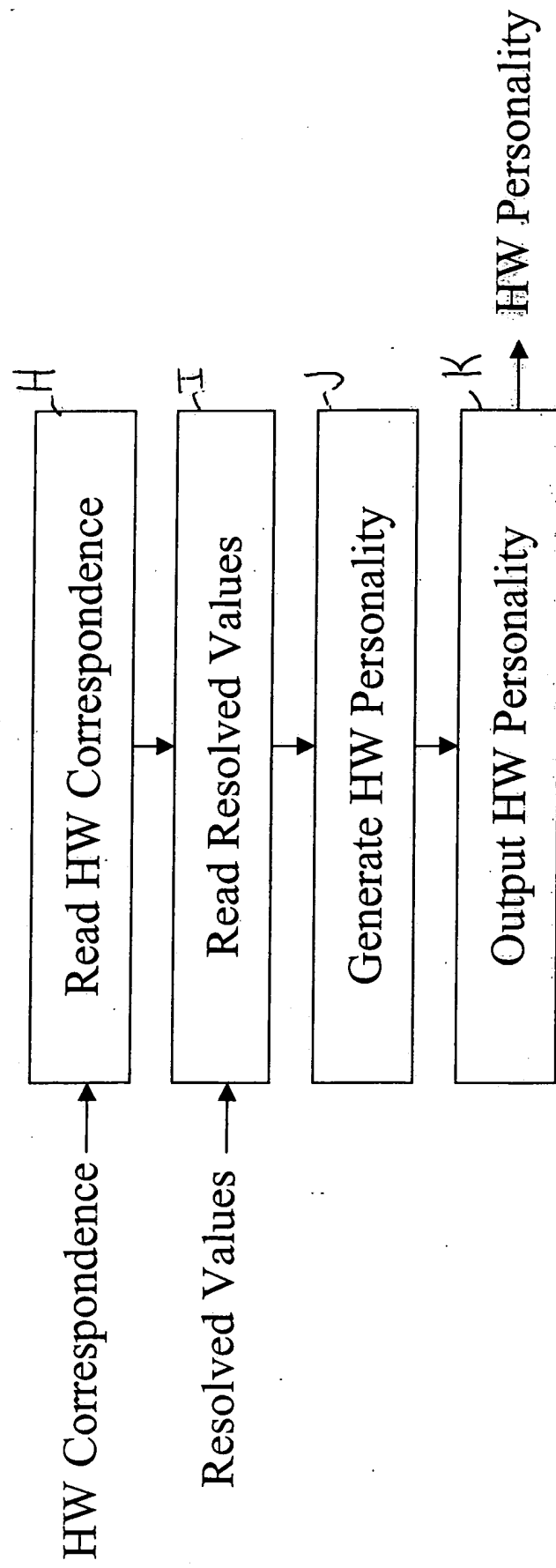


Figure 7

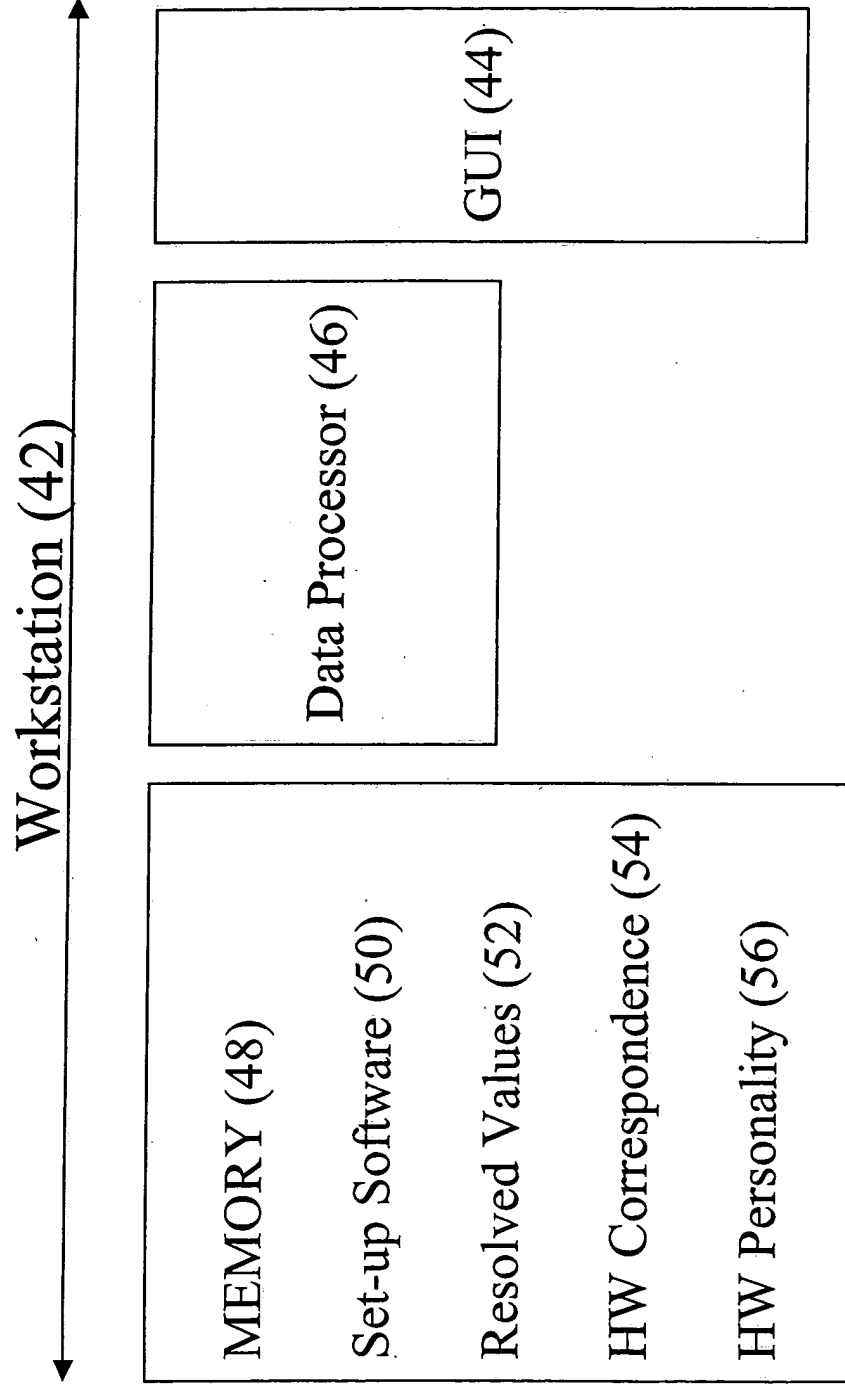


Figure 8

Figure 1 Example

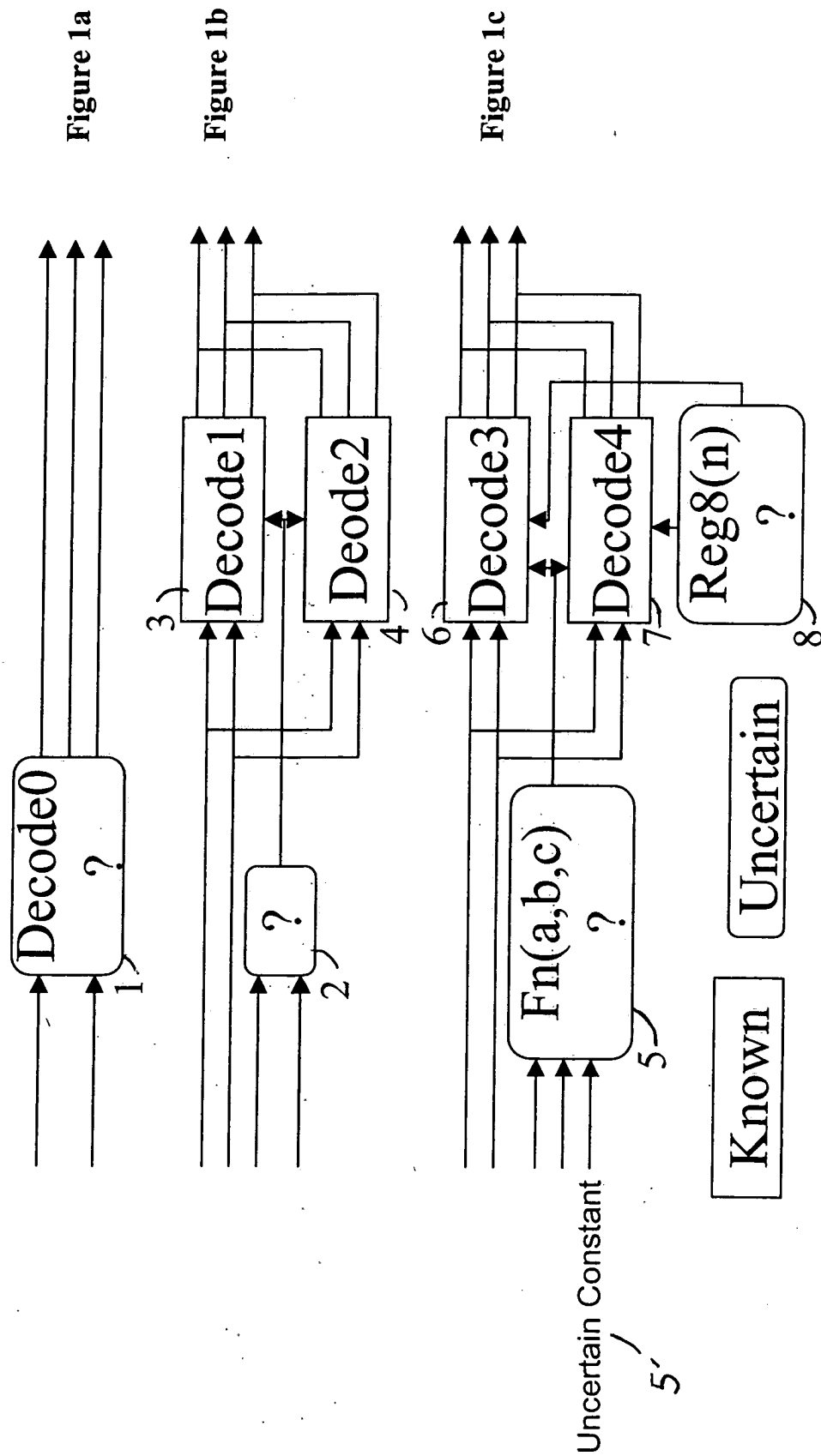


Figure 2 First Mapping (Steps A,B)

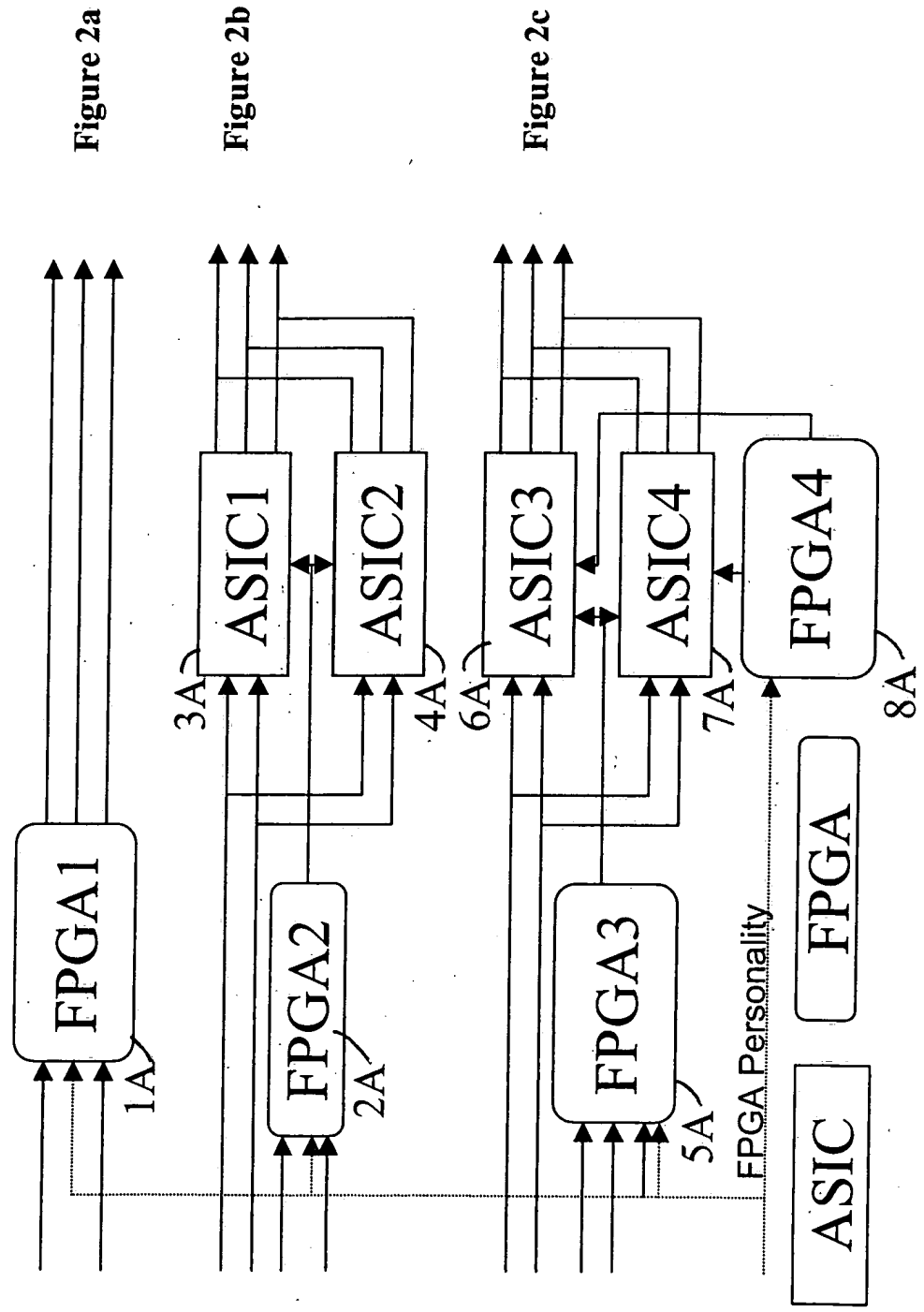


Figure 3 (Steps C,D,E)

